



vc / cv

rab / bit



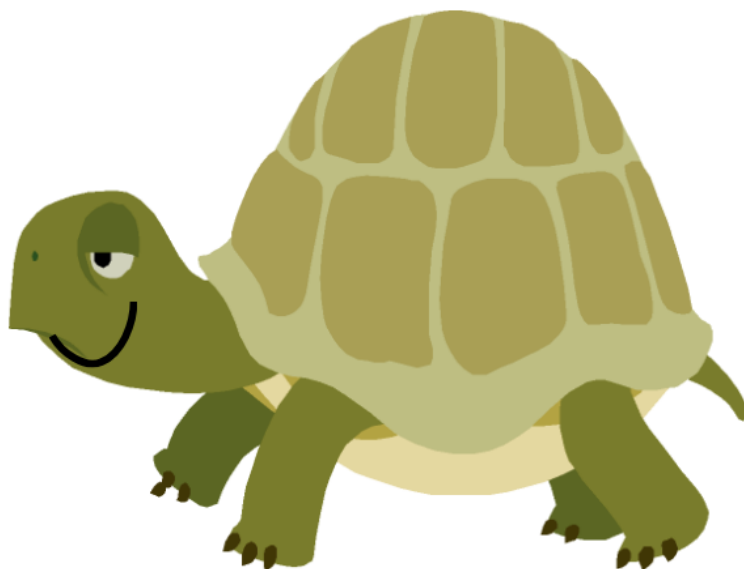
v/cv

ti/ger



vc/v

rob/in



--/cle



3 2 1

tur/tle



v / v

li / on



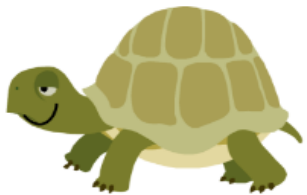
vc/cv
rab/bit



v/cv
ti/ger



vc/v
rob/in



--/cle
←
3 2 1
tur/tle



v/v
li/on

