



vc/cv

rab/bit



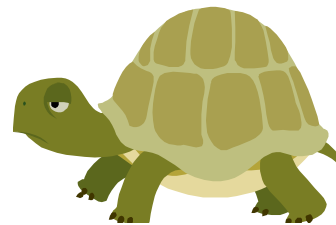
v/cv

ti/ger



vc/v

rob/in



-/cle

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v/v

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